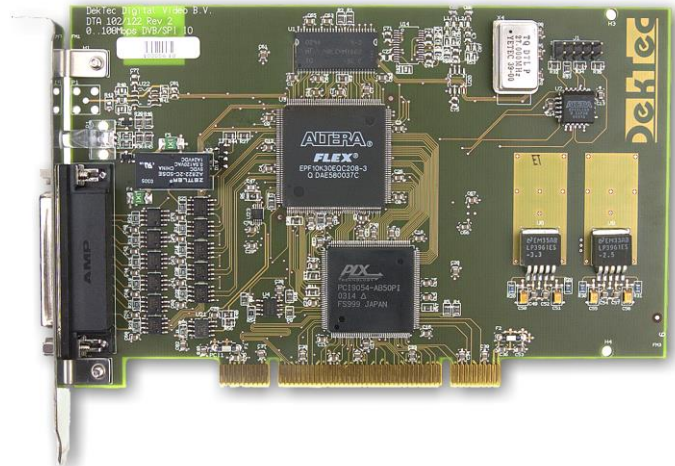


# DVB-SPI (LVDS) Input Adapter for PCI Bus

- ❑ Clock rate from 0 to 108 Mbit/s
- ❑ 8-MBytes on-board buffer
- ❑ Support for external signal adapters

## FEATURES

- High-speed Transport-Stream input, compliant to DVB-SPI (Synchronous Parallel Interface) as defined in DVB BlueBook A010 rev 1 and EN50083-9
- Special support for external signal adapters from e.g. ECL or TTL
- Input bitrate 0 .. 108 Mbit/s
- LED indicator shows synchronisation status
- 8-Mbytes on-board buffer
- Optional FEC stripping
- Support for 188-byte and 204-byte packets, or arbitrary packet size in raw mode
- On-board PCI Bus master for off-loading host processor
- DMA burst-mode transfers for optimal usage of PCI Bus
- PCI rev 2.2, 32 bit, 33 MHz
- Comes with free:
  - Windows-XP/2k3/Vista & Linux device driver and Software Development Kit for developing custom applications
  - Example source code for TS recorder



## APPLICATIONS

- General-purpose adapter for reading DVB-SPI transport streams into a PC
- DVB-SPI transport-stream recording
- With DTC-320 *StreamXpert* software: Versatile DVB-SPI transport-stream analysis and monitoring tool

## KEY ATTRIBUTES

Parameter	Value
Physical layer	DVB-SPI (LVDS)
DVB-SPI connector	25-pin sub-D
DVB-SPI clock	0 .. 13.5MHz
Receive bitrate	0 .. 108Mbit/s
Packet size in bytes	188 or 204*
Target adapter power	5V, 2A

\* Arbitrary packet size in raw mode

## RELATED PRODUCTS

Type	Description
DTA-102	DVB-SPI output adapter for PCI
DTE-CA25	1.5m or 2.5m DVB-SPI cable

## ORDERING INFORMATION

Type	Description
DTA-122-SL	DVB-SPI input adapter for PCI bus with <i>StreamXpert Lite</i>
DTA-122-SX	DTA-122-DP with <i>StreamXpert</i>

Please refer to [www.dektec.com](http://www.dektec.com) for the latest pricing and a list of distributors and resellers.